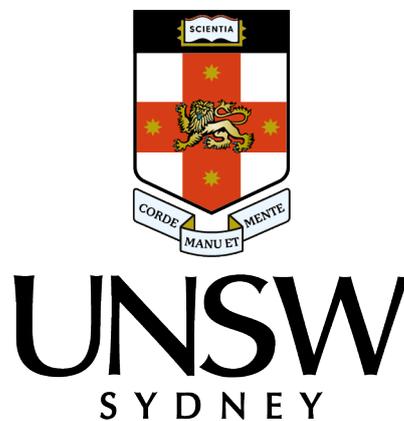


Thesis Topic

Firstname Lastname

A thesis in fulfilment of the requirements for the degree of
<degree name, i.e. one of Doctor of Philosophy/Professional
Doctorate/Master by Research/Master of Philosophy>



School of Computer Science and Engineering

Faculty of Engineering

The University of New South Wales

November 2026

Abstract

The abstract.

Acknowledgement

This section should not exceed one page.

Scholarship and/or Funding - Required

Candidates in receipt of an Australian Government RTP stipend or RTP fees offset must acknowledge the Australian Government's contribution during and after completion of the HDR program. The acknowledgement must include the mention of support through the Australian Government Research Training Program as follows:

This research was supported by the Commonwealth through an Australian Government Research Training Program Scholarship [DOI: <https://doi.org/10.82133/C42F-K220>].

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List of Publications and Presentations

List of Publications

- paper 1
- paper 2

List of Presentations

Oral presentations:

- presentation 1

Poster presentations:

- presentation 2

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Abbreviations

CPU	Central Processing Unit
CSE	Computer Science and Engineering
CUDA	Compute Unified Device Architecture
DRAM	Dynamic Random-Access Memory

Chapter 1

Introduction

The introduction.

1.1 Section

1.1.1 Subsection

1.1.1.1 Subsubsection

- item 1¹
- item 2
- item 3

¹A footnote

Chapter 2

Literature Review

The literature review. This is a citation [1].

Chapter 3

Technical Chapter

Please change the chapter title to better reflect and clearly summarise the contributions of this chapter.

Lorem ipsum dolor sit amet, consectetur adipiscing elit, sed do eiusmod tempor incididunt ut labore et dolore magna aliqua. Ut enim ad minim veniam, quis nostrud exercitation ullamco laboris nisi ut aliquip ex ea commodo consequat. Duis aute irure dolor in reprehenderit in voluptate velit esse cillum dolore eu fugiat nulla pariatur. Excepteur sint occaecat cupidatat non proident, sunt in culpa qui officia deserunt mollit anim id est laborum.

Algorithm 1 shows ...

Algorithm 1 An algorithm

1: **function** COMPUTATION(a, b, ref, b, d)

2: $a \leftarrow b + c + d$

▷ Comment

3: **end function**

Chapter 4

Technical Chapter

Please change the chapter title to better reflect and clearly summarise the contributions of this chapter.

Equation 4.1 is for ...

$$A = (B_1 + \mu B_2) \sum_{i=0}^{n-1} r[i] \quad (4.1)$$

Table 4.1 demonstrates ...

Table 4.1: A table

column1	column2
a	10
b	14
c	30
d	1.7
e	4.6

Chapter 5

Technical Chapter

Please change the chapter title to better reflect and clearly summarise the contributions of this chapter.

The Fig. 5.1 in ...

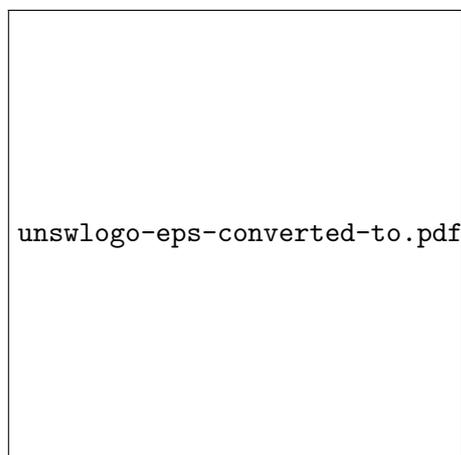


Figure 5.1: The UNSW logo

A figure with sub-figures are in Fig. 5.2.

Chapter 6

Conclusion and Future Directions

Appendix A

Supplementary Materials

A hello world C program:

```
1 #include <stdio.h>
2 int main(){
3     fprintf(stderr, "Hello world\n");
4     return 0;
5 }
```

Compile and run as follows:

```
1 gcc -Wall a.c -o prog
2 ./prog
```

Appendix B

Documentation

References

- [1] D. Adams, *The Hitchhiker's Guide to the Galaxy*. San Val, 1995. [Online]. Available:
<http://books.google.com/books?id=W-xMPgAACAAJ>